

06 April 2026



CHERI

Towards Heterogeneous CHERI Compartmentalization

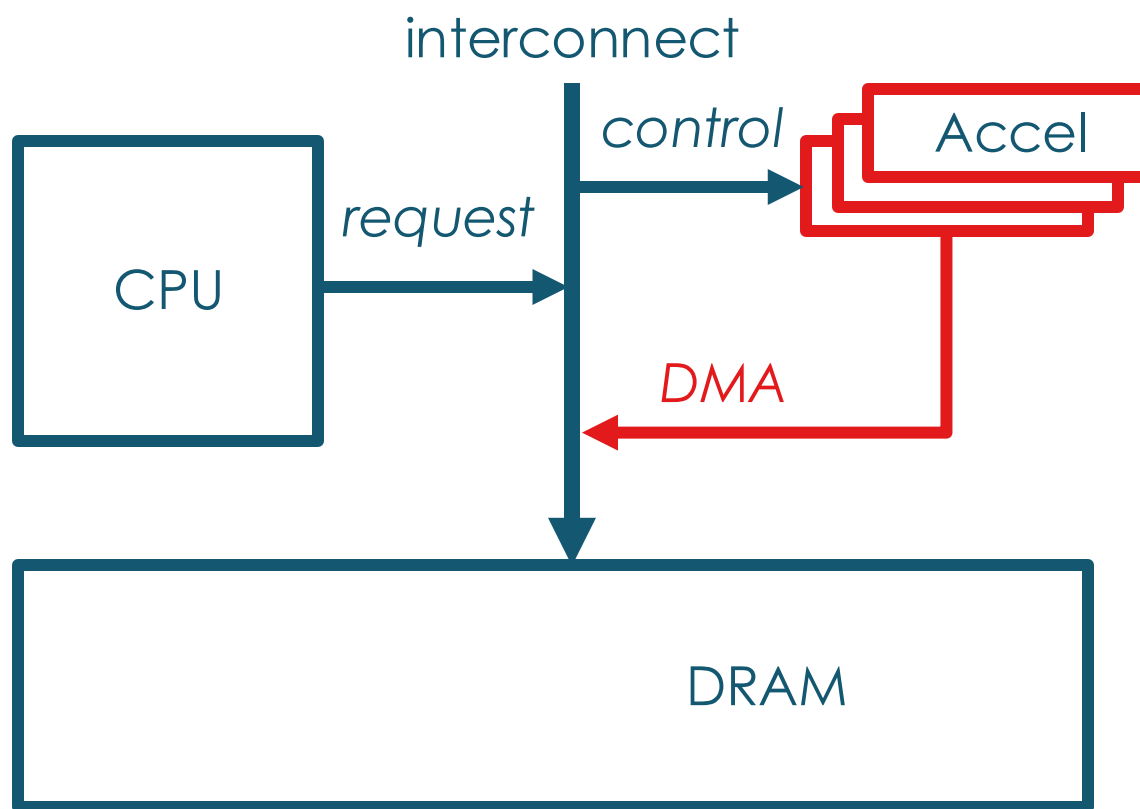
Jianyi Cheng

Assistant Professor – University of Edinburgh



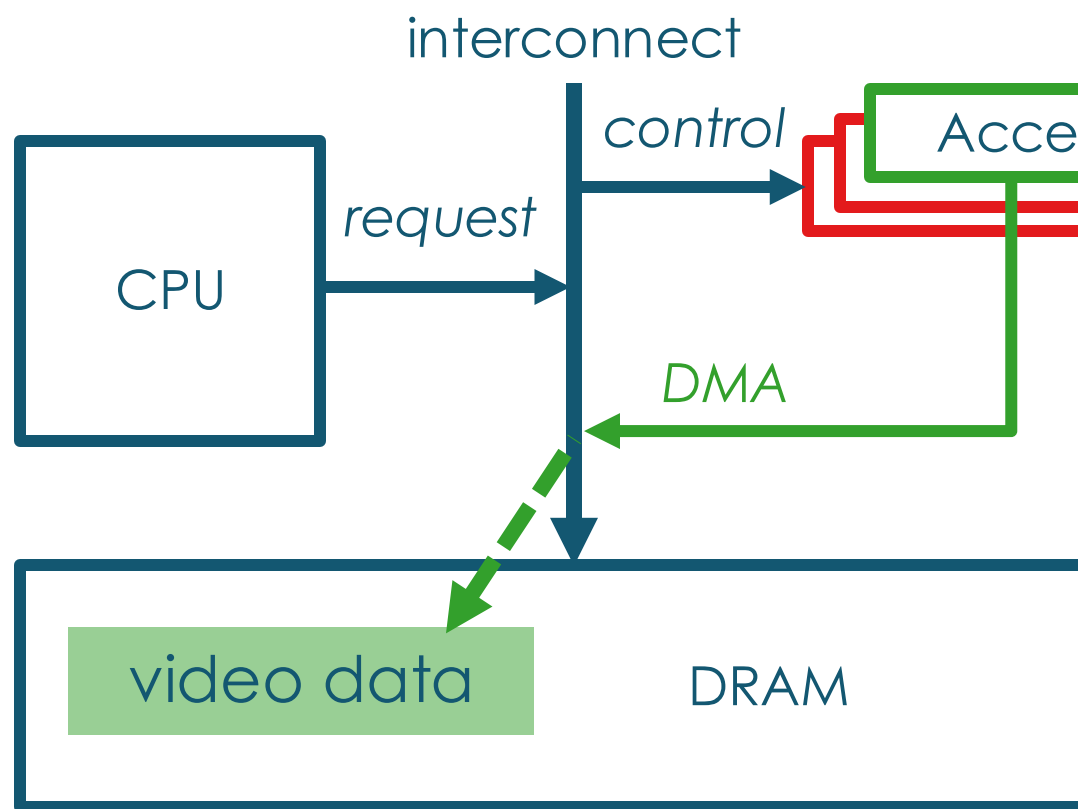
○ Accelerators are good but...

- ◆ Attack vectors: malicious use of accelerators



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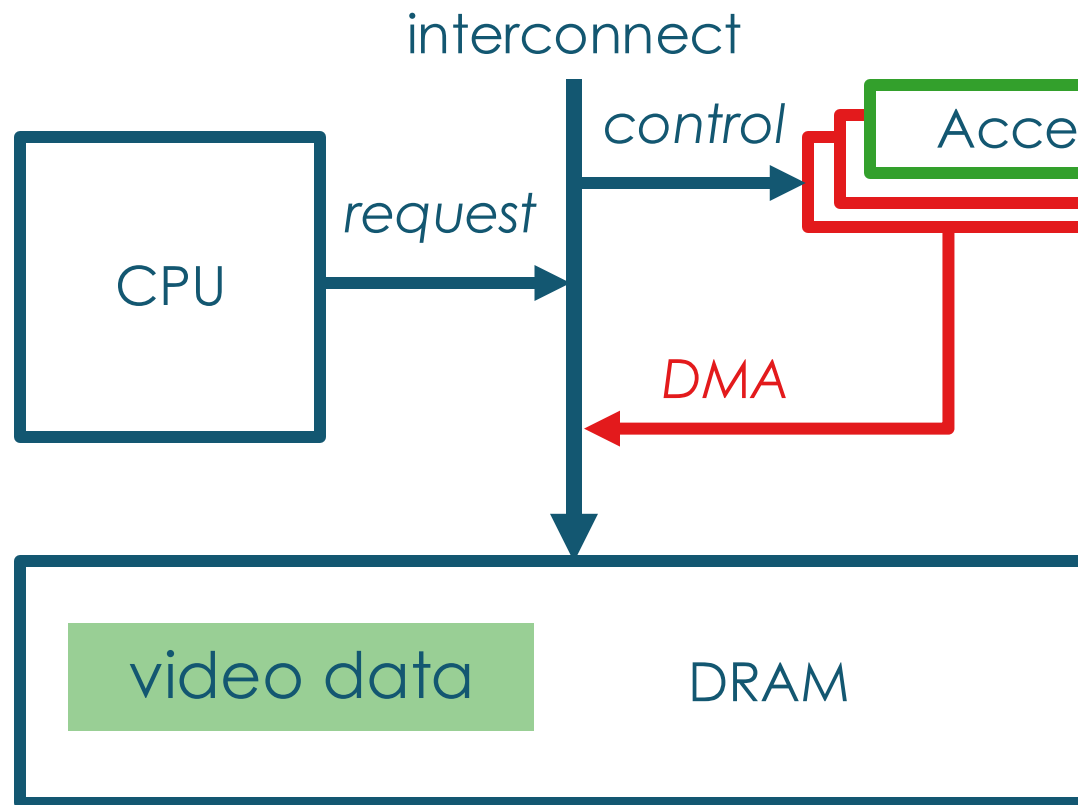
- ◆ Attack vectors: malicious use of accelerators



david

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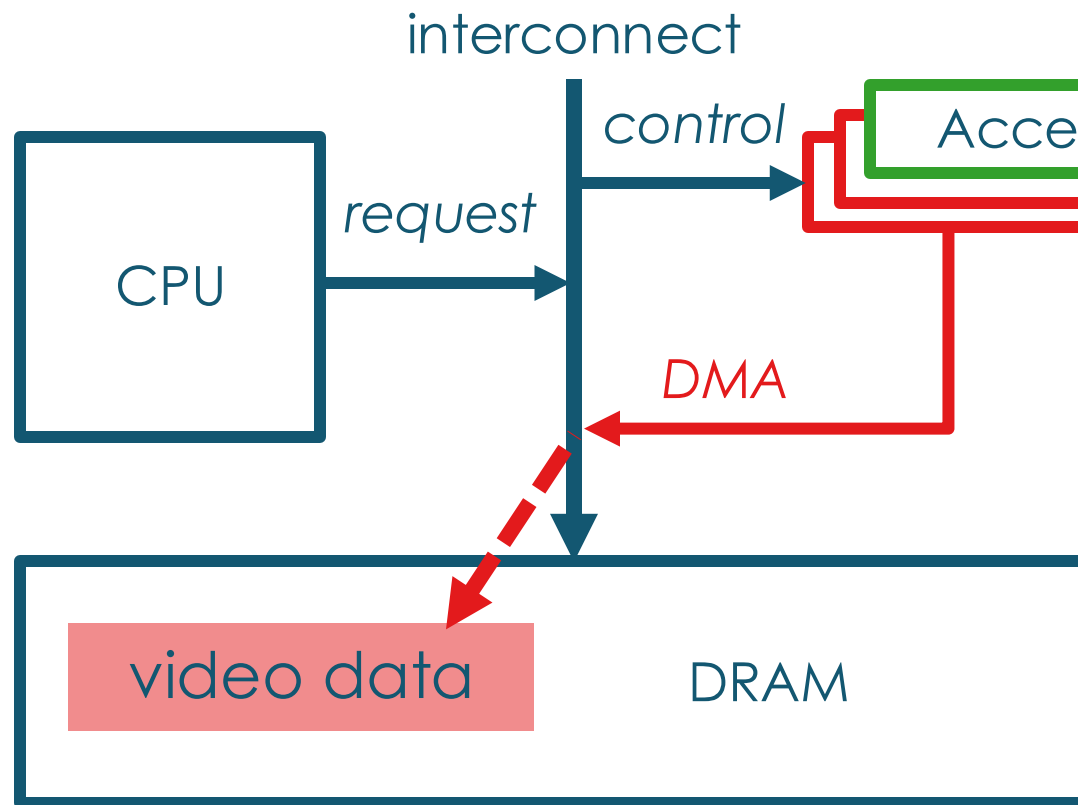


dAV1d



○ Accelerators are good but...

- ◆ Attack vectors: malicious use of accelerators



dAV1d





○ Accelerators are good but...

- ◆ Attack vectors: malicious use of accelerators

interconnect

Capsicum: practical capabilities for UNIX

Robert N. M. Watson
University of Cambridge

Jonathan Anderson
University of Cambridge

Ben Laurie
Google UK Ltd.

Kris Kennaway
Google UK Ltd.

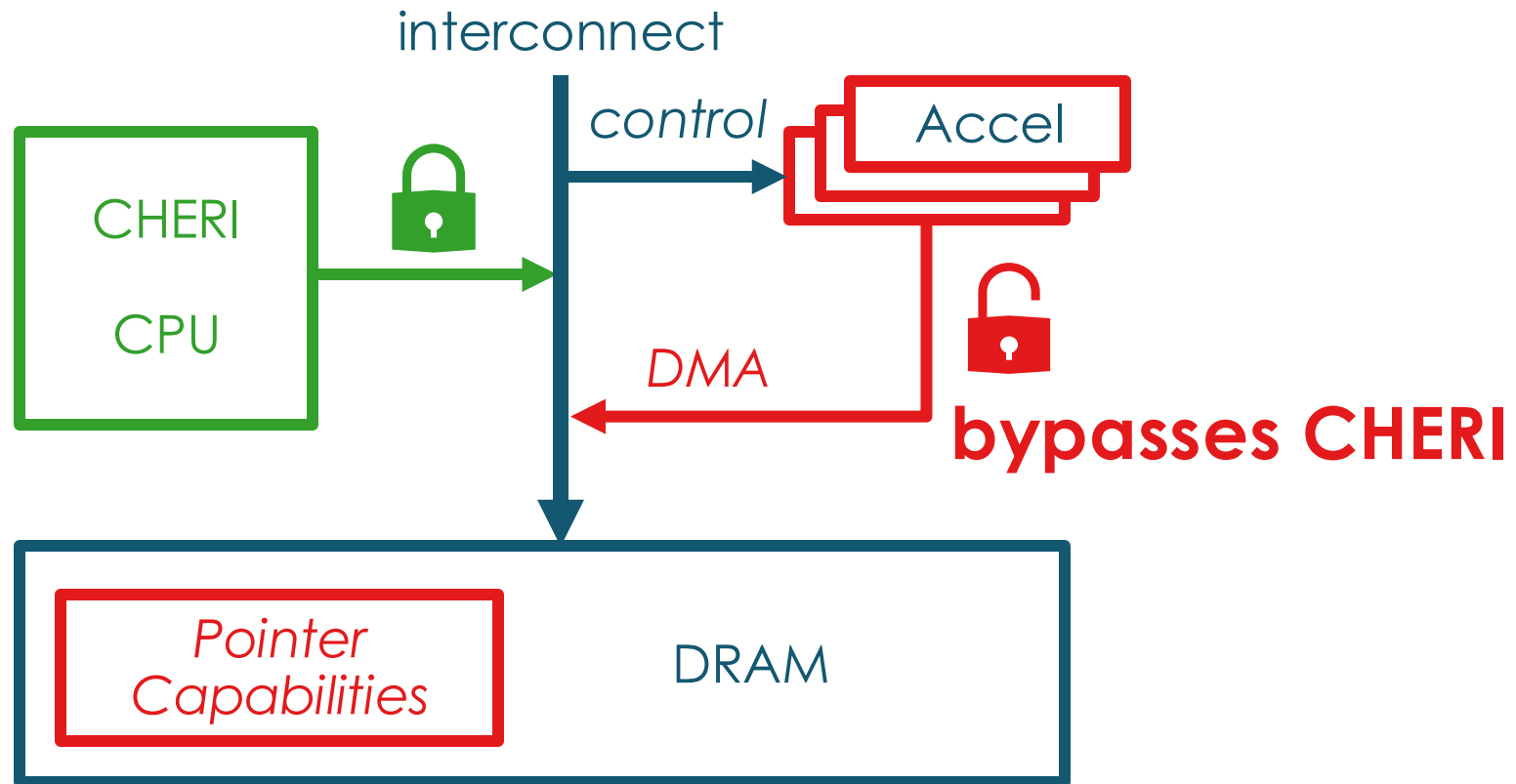
Software solutions
are complex...

How about
hardware
solutions?



○ Memory safety issues are concerning

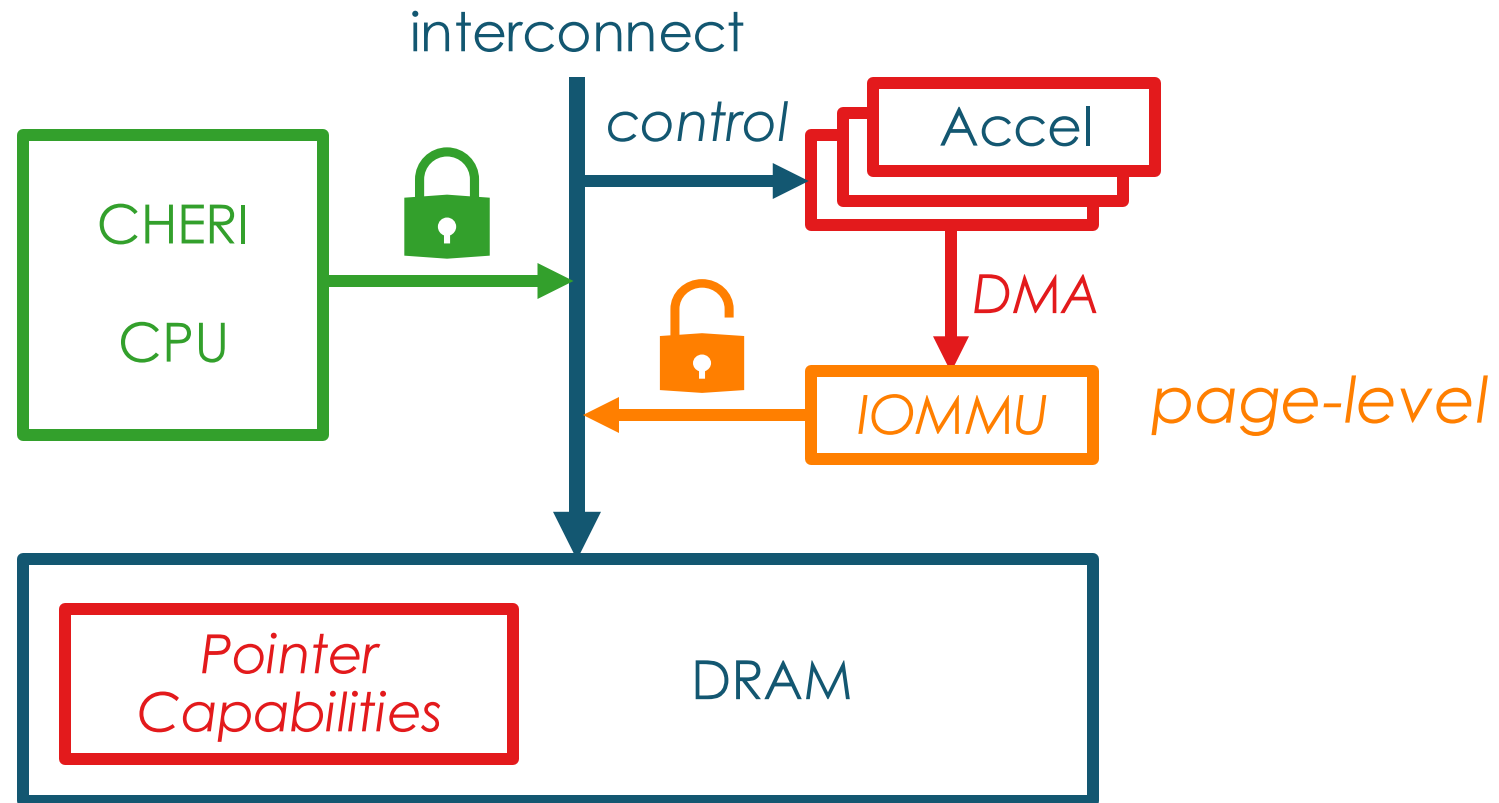
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Memory safety issues are concerning

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Memory safety issues are concerning

- Attack vectors: malicious use of accelerators
2024 ACM/IEEE 51st Annual International Symposium on Computer Architecture (ISCA)

sNPU: Trusted Execution Environments on Integrated NPUs

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[‡]*Department of Precision Instrument, Tsinghua University*

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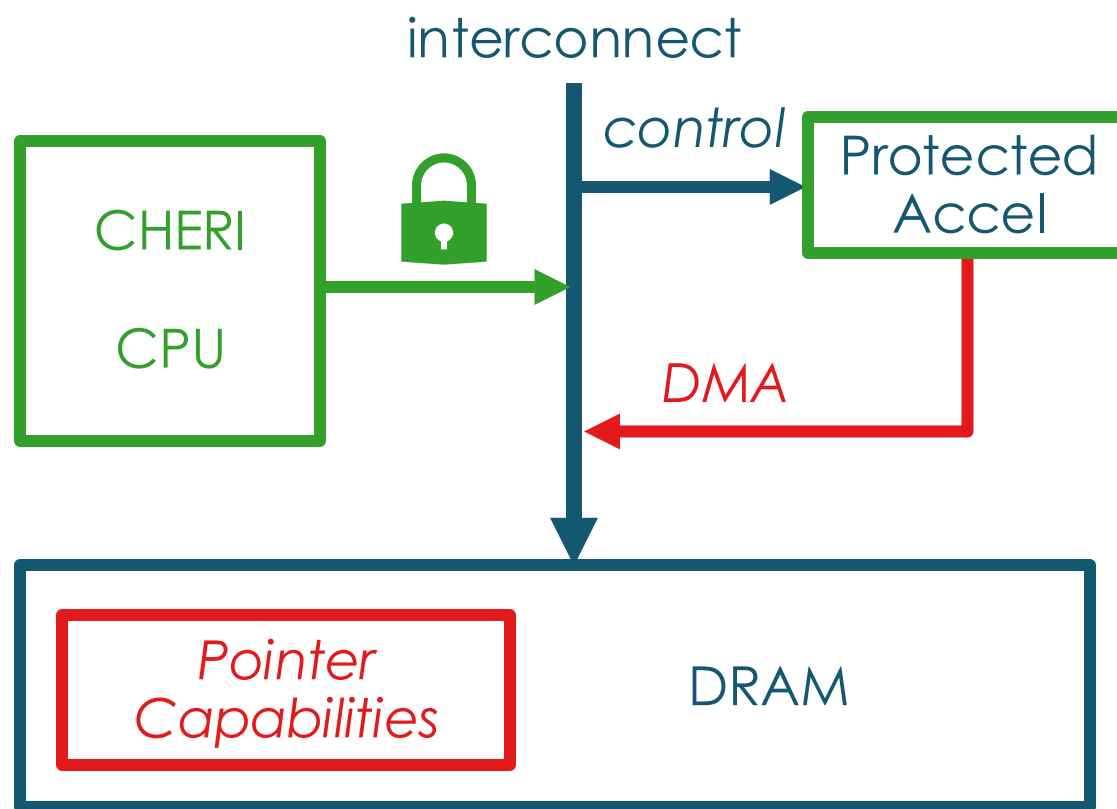
[§]*Key Laboratory of System Software (Chinese Academy of Sciences)*





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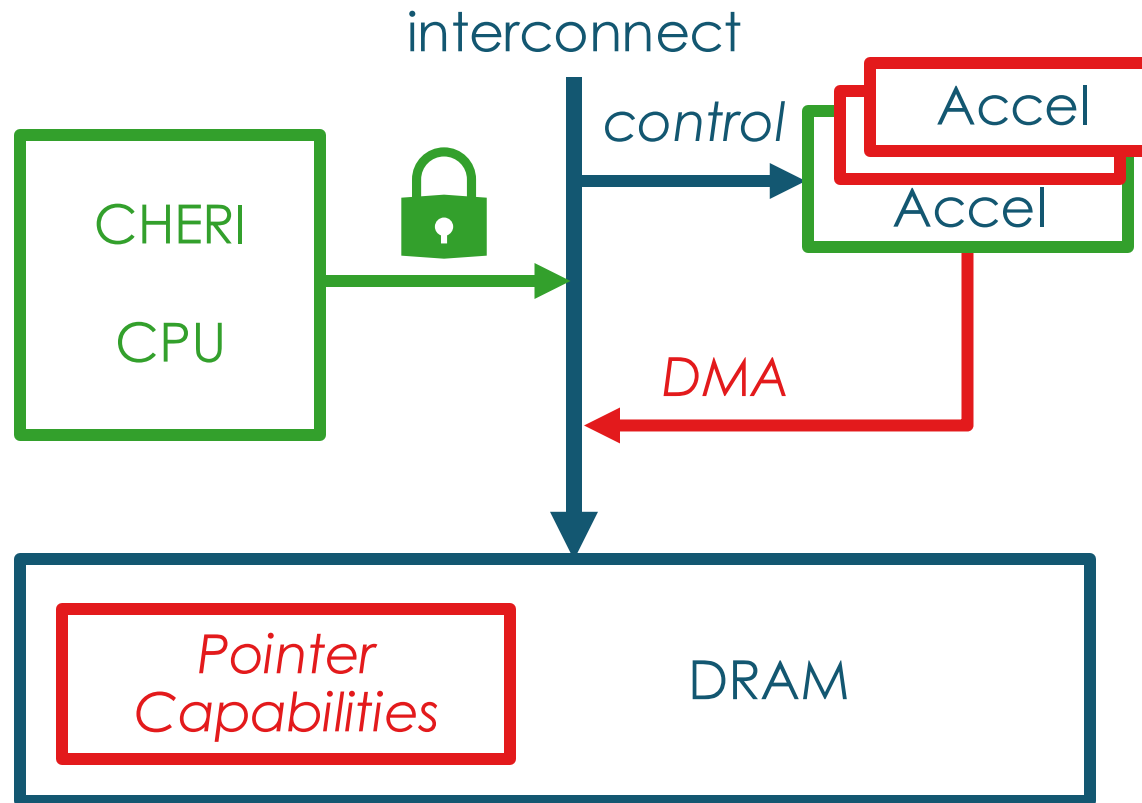
- ◆ Attack vectors: malicious use of accelerators





○ Memory safety issues are concerning

- ◆ Attack vectors: malicious use of accelerators





○ Research Challenges

Coarse Granularity

IOMMUs provide memory protection at *the page level*.

Protection Heterogeneity

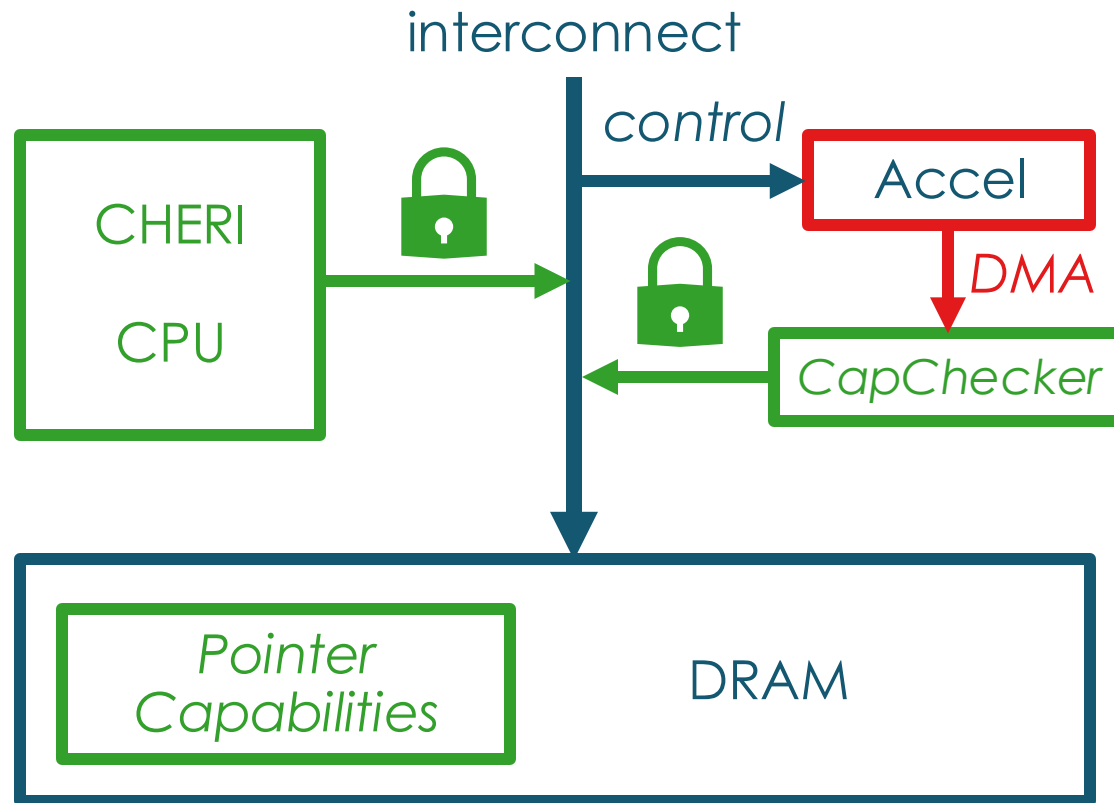
Different targets speak *different policies* for protection.

Our Goals

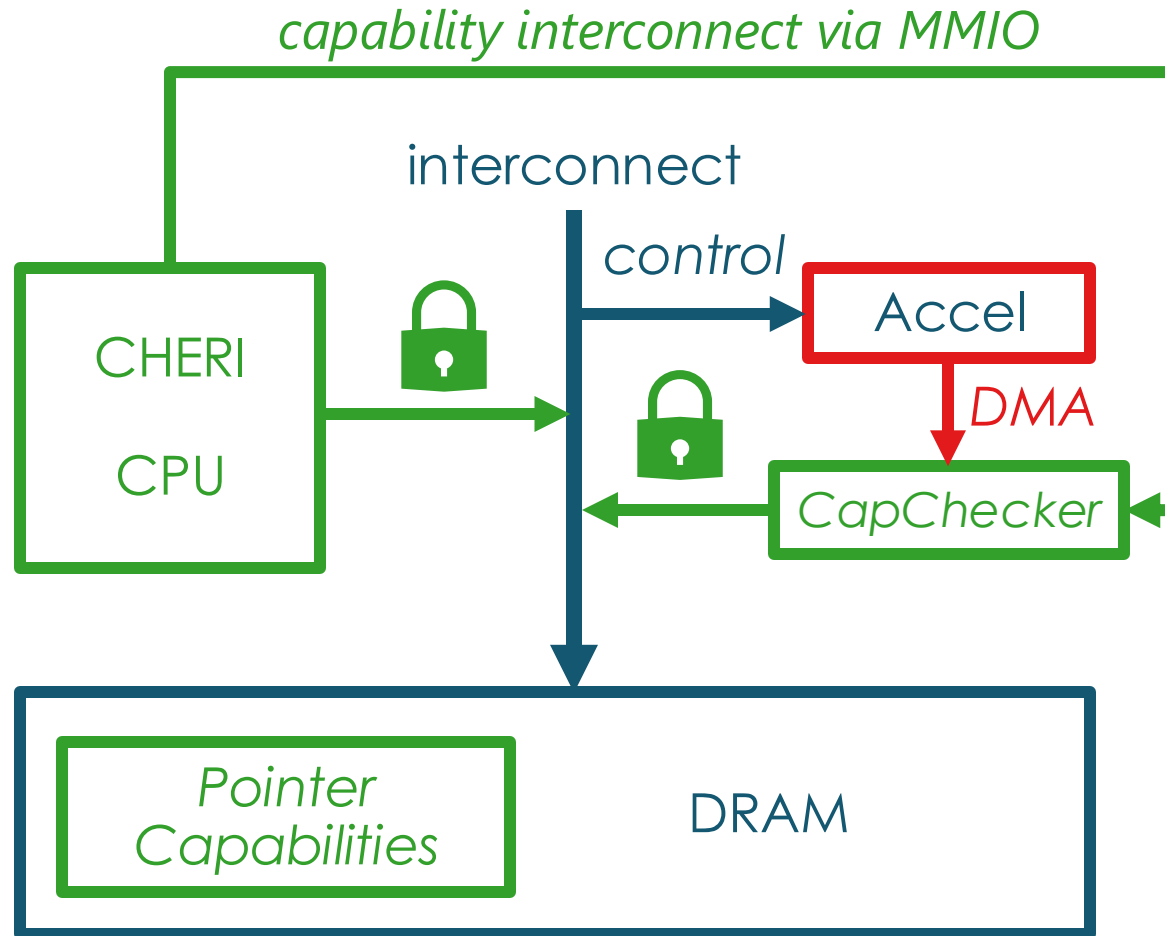
Fine Granularity

Accelerator Independent

○ CapChecker: Hardware Capability Checker



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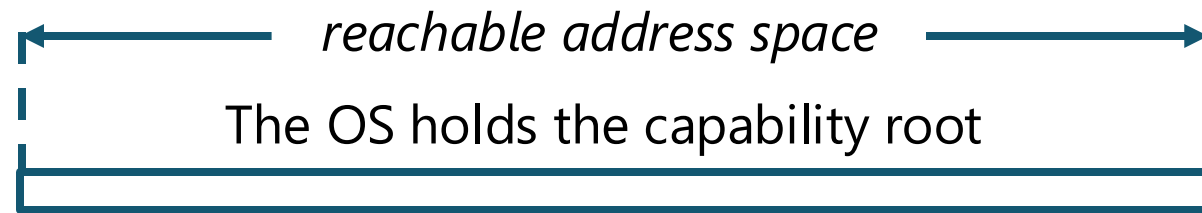


○ Threat Model

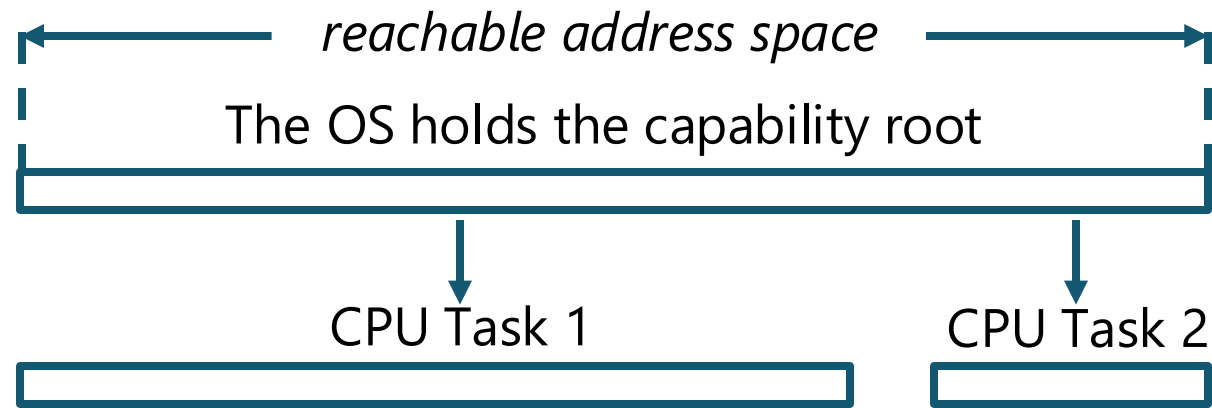


- CPU is already protected by CHERI
- No dynamic memory management on accelerators
- Software components are trustworthy.

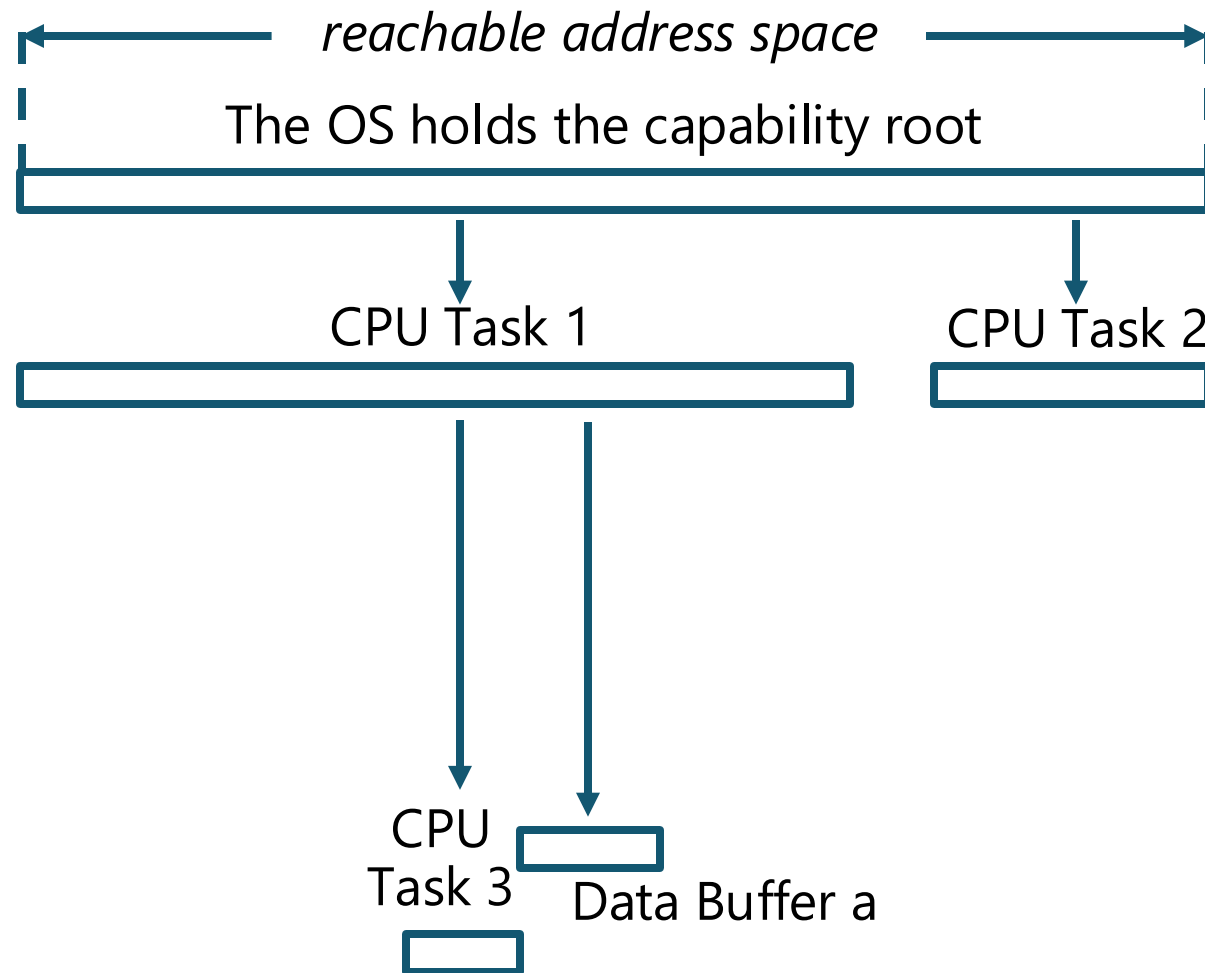
○ Capability Model



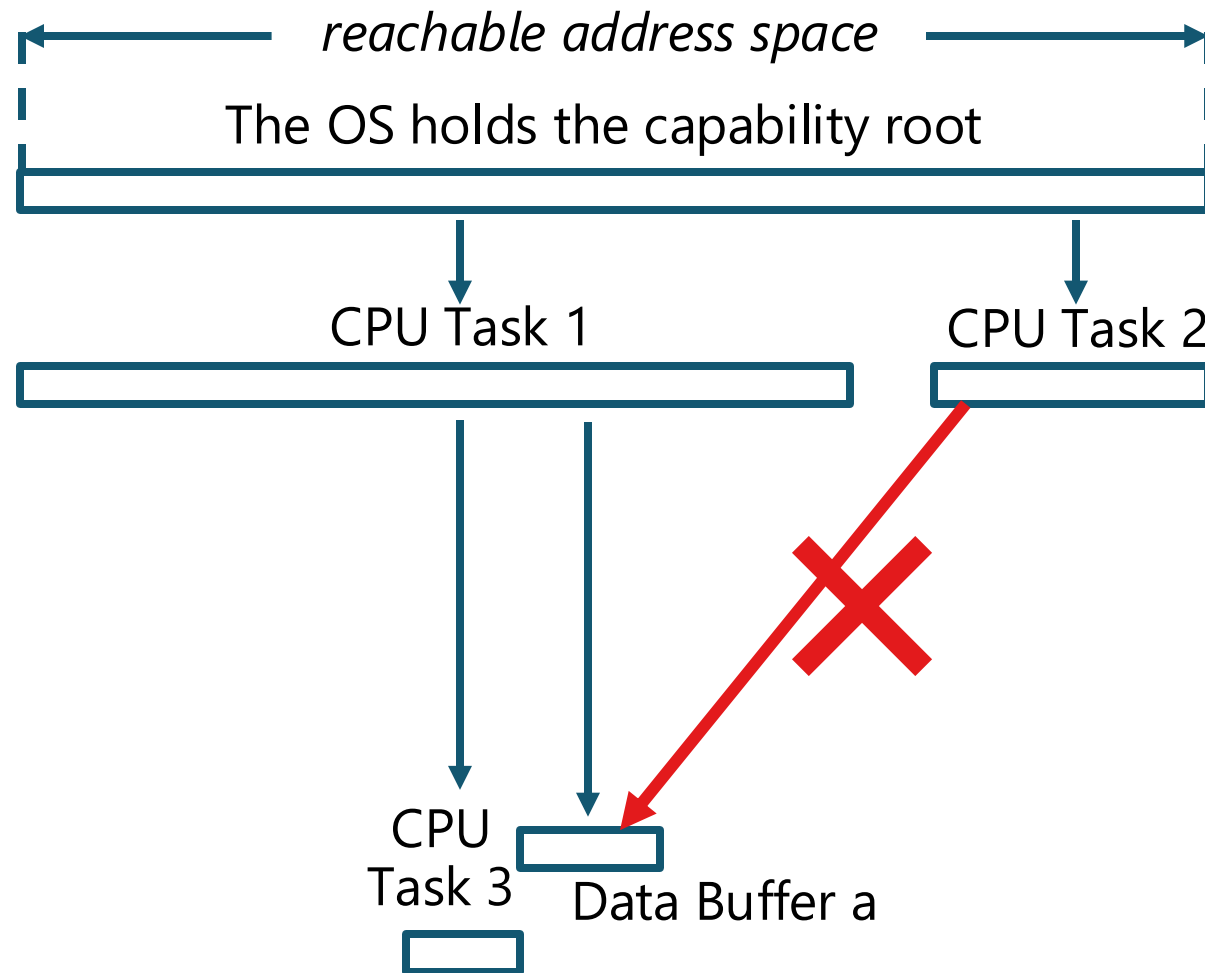
○ Capability Model



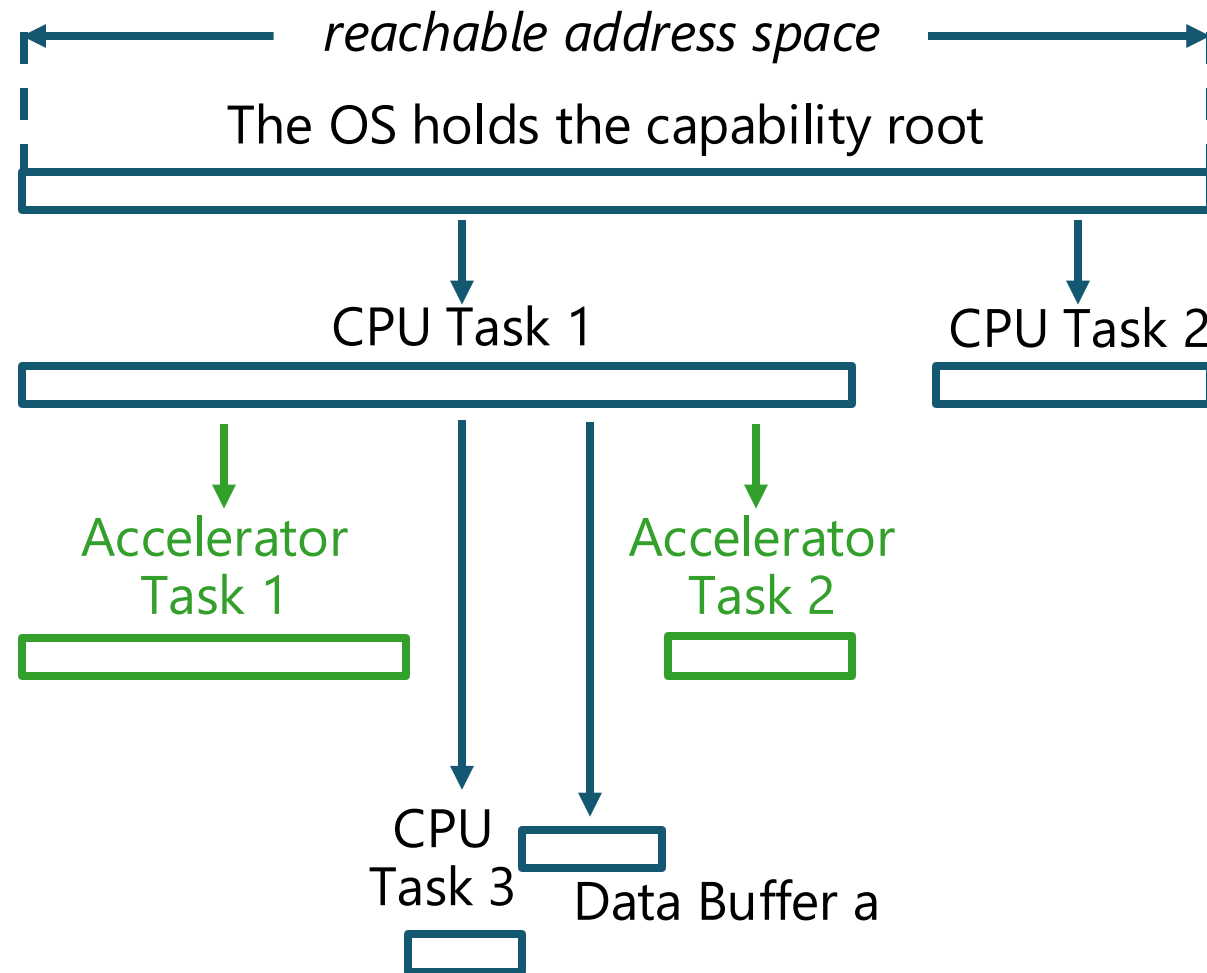
○ Capability Model



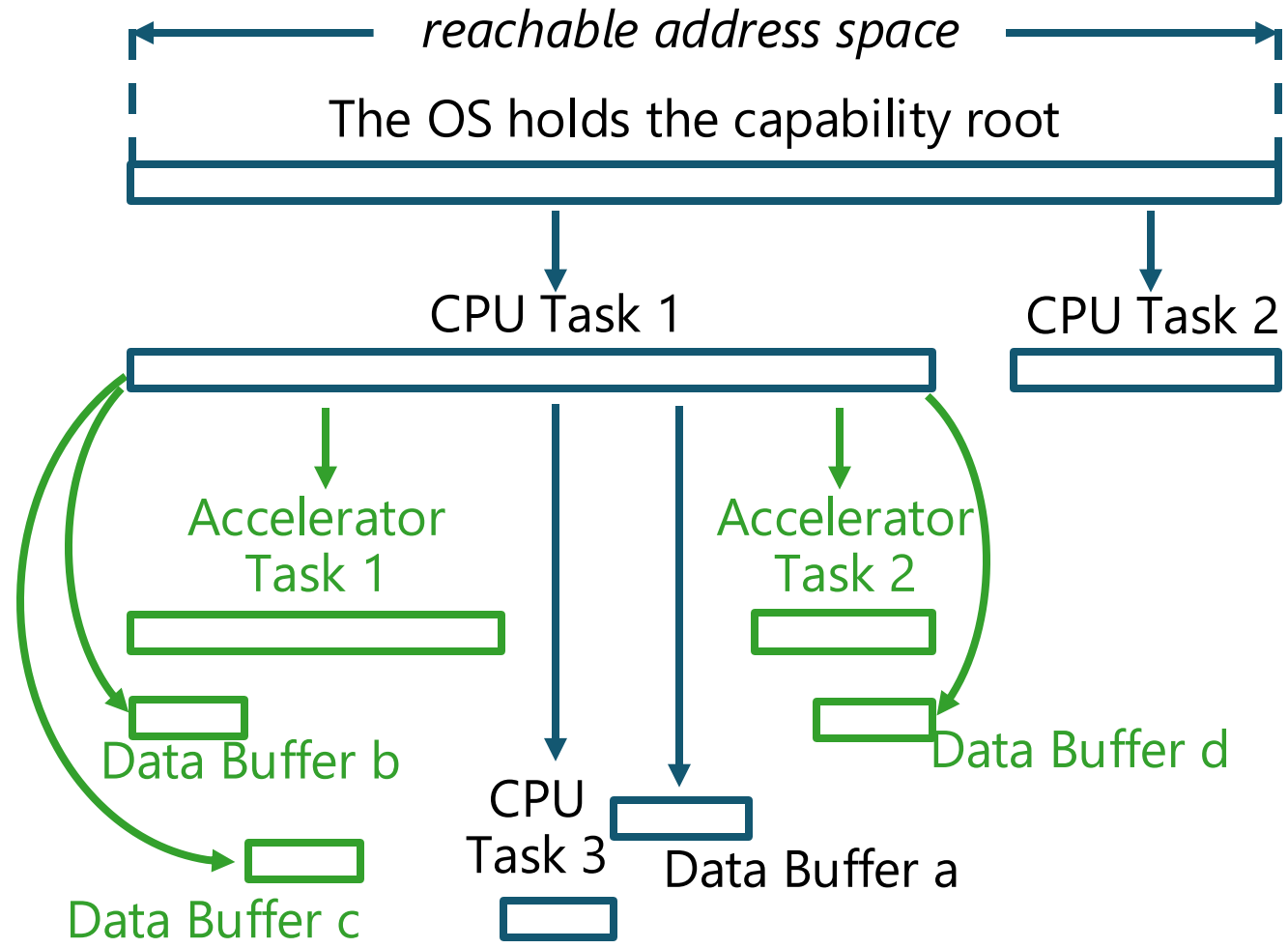
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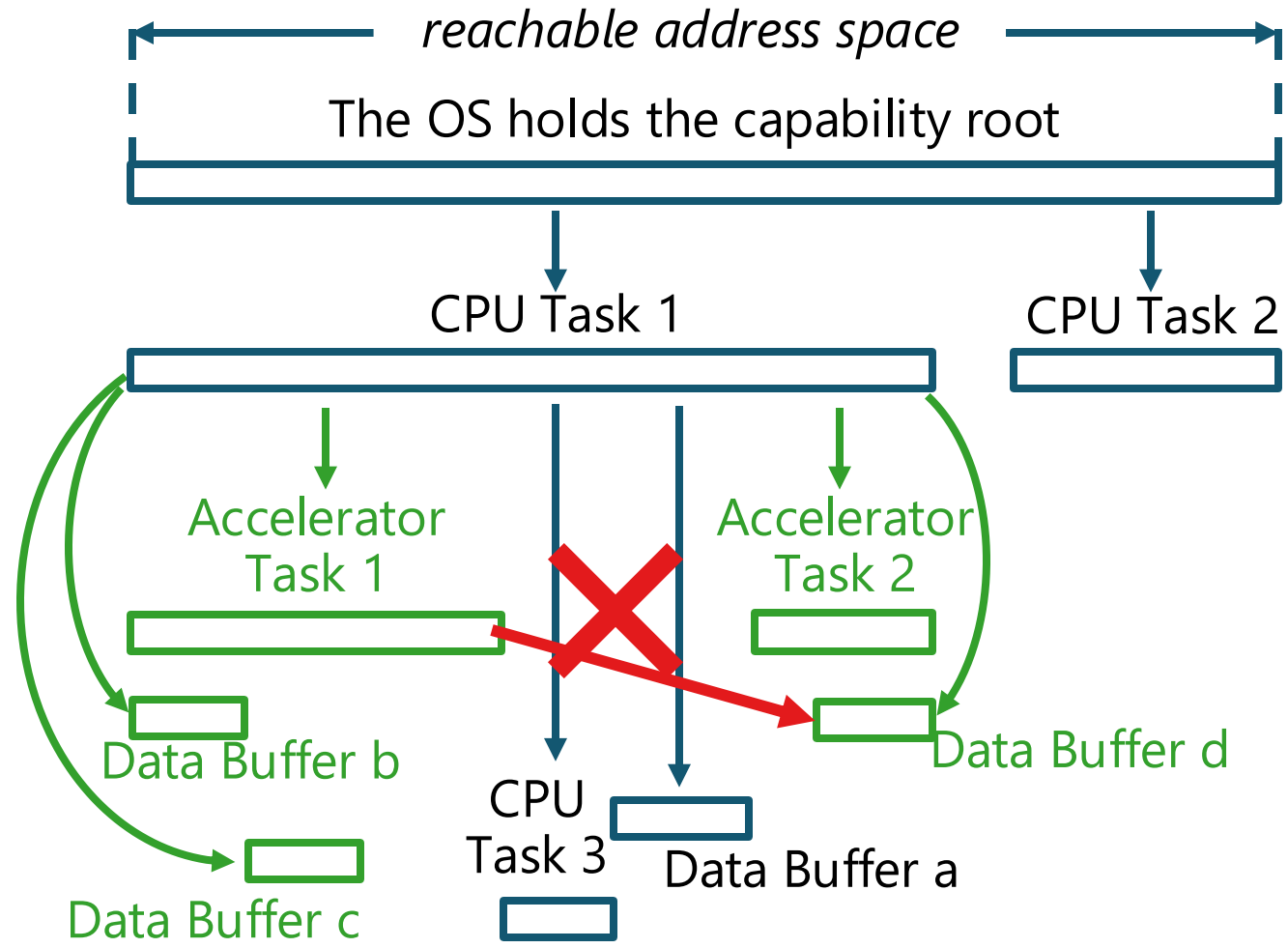
○ Capability Model



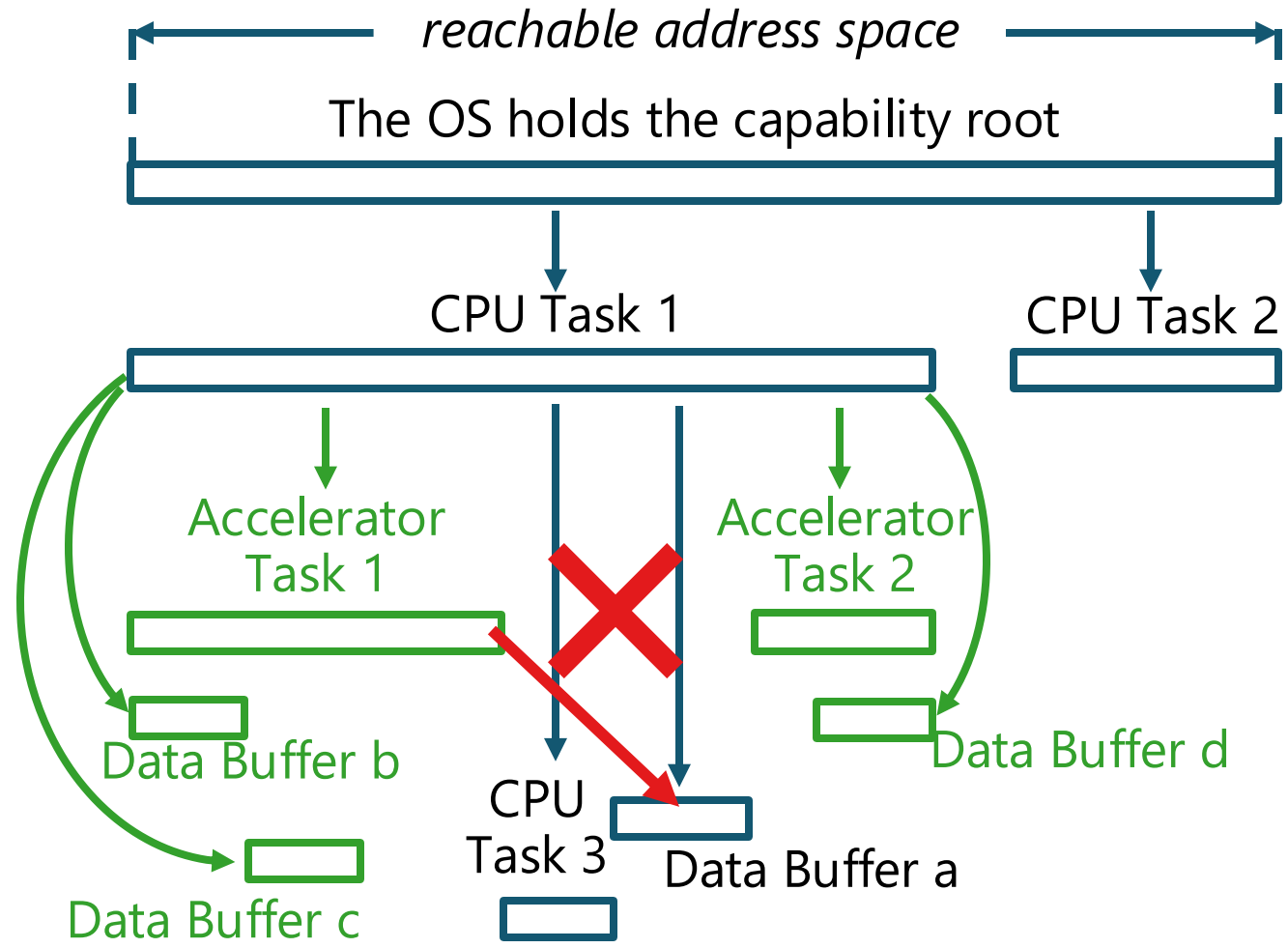
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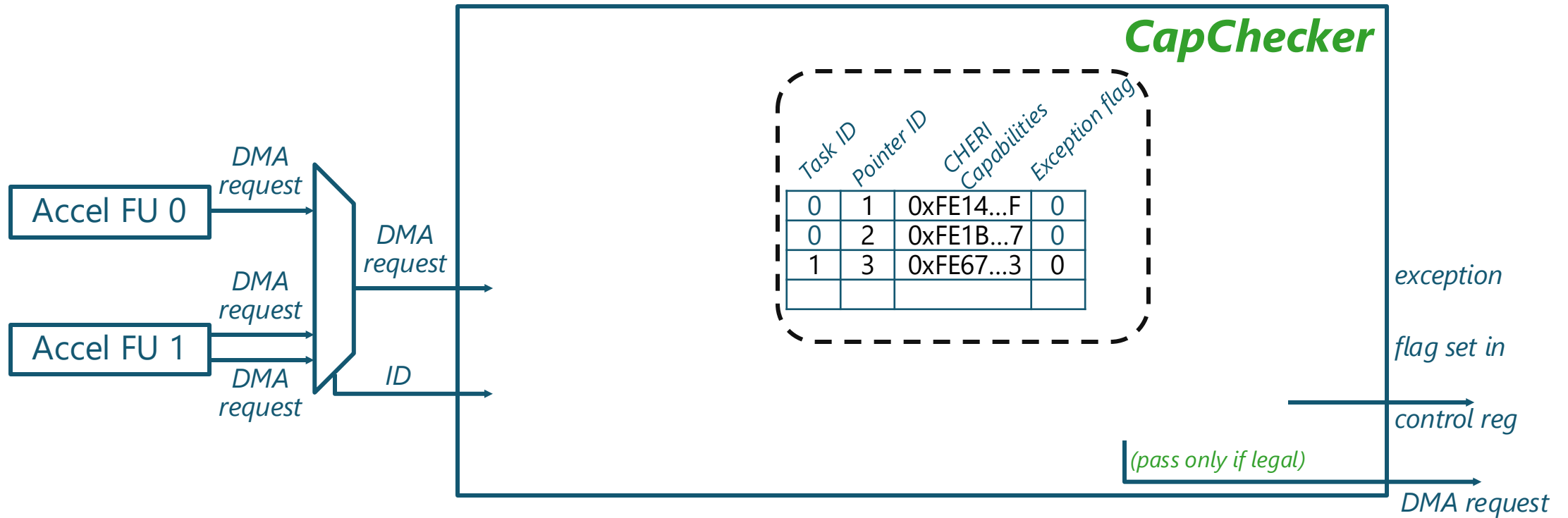




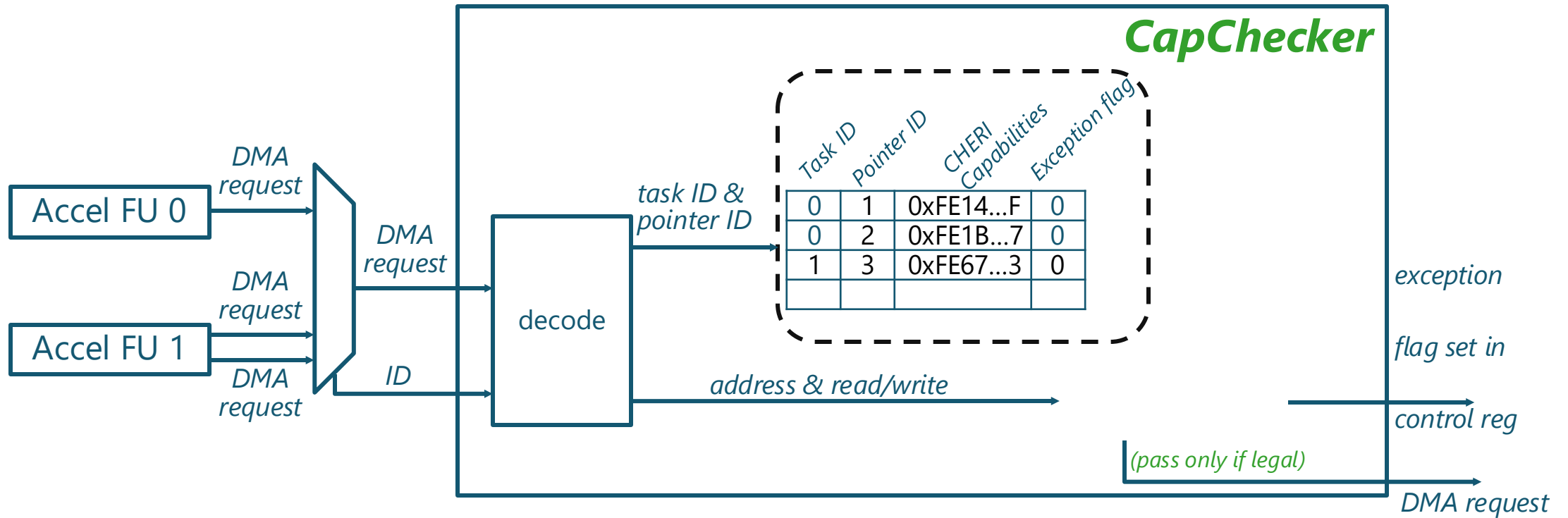
○ CapChecker Hardware Design



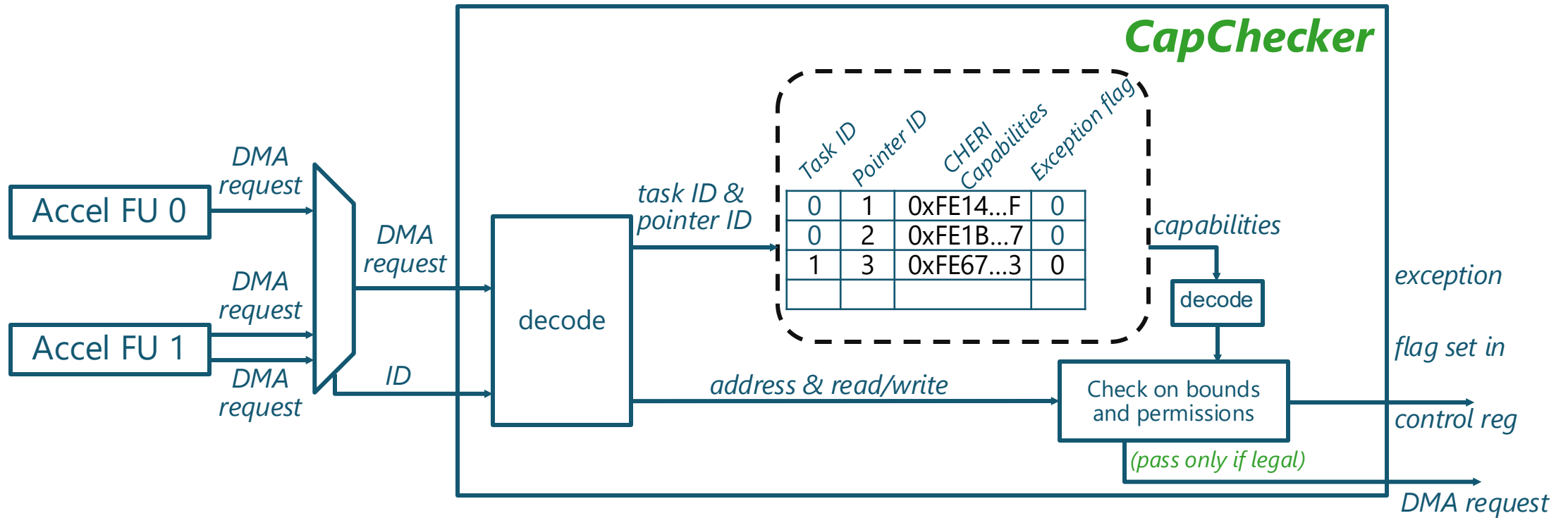
CapChecker Hardware Design



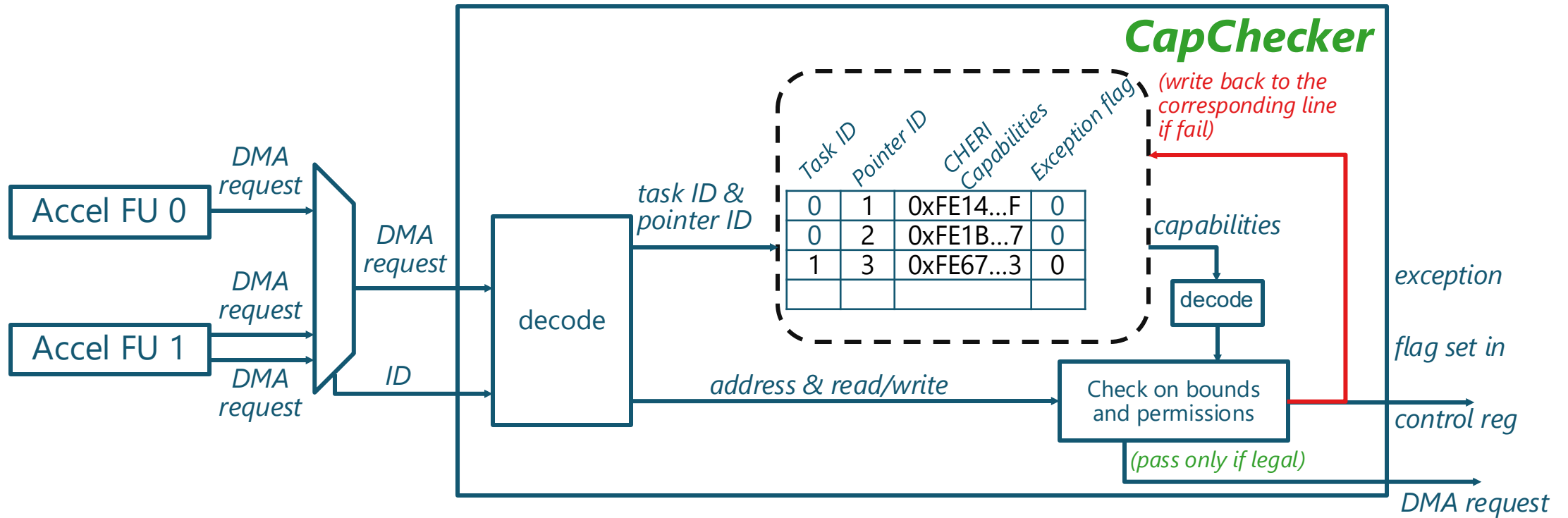
CapChecker Hardware Design



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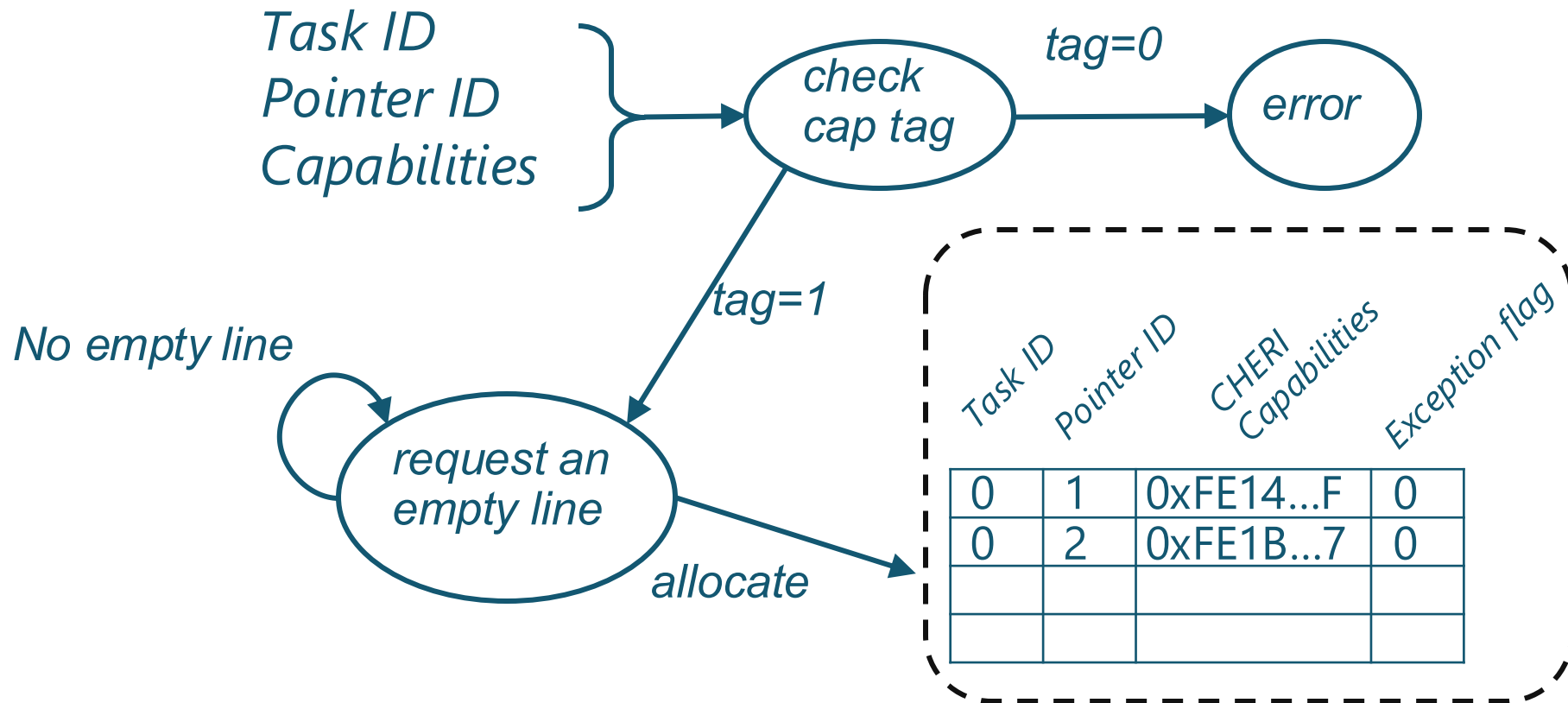


CapChecker Hardware Design



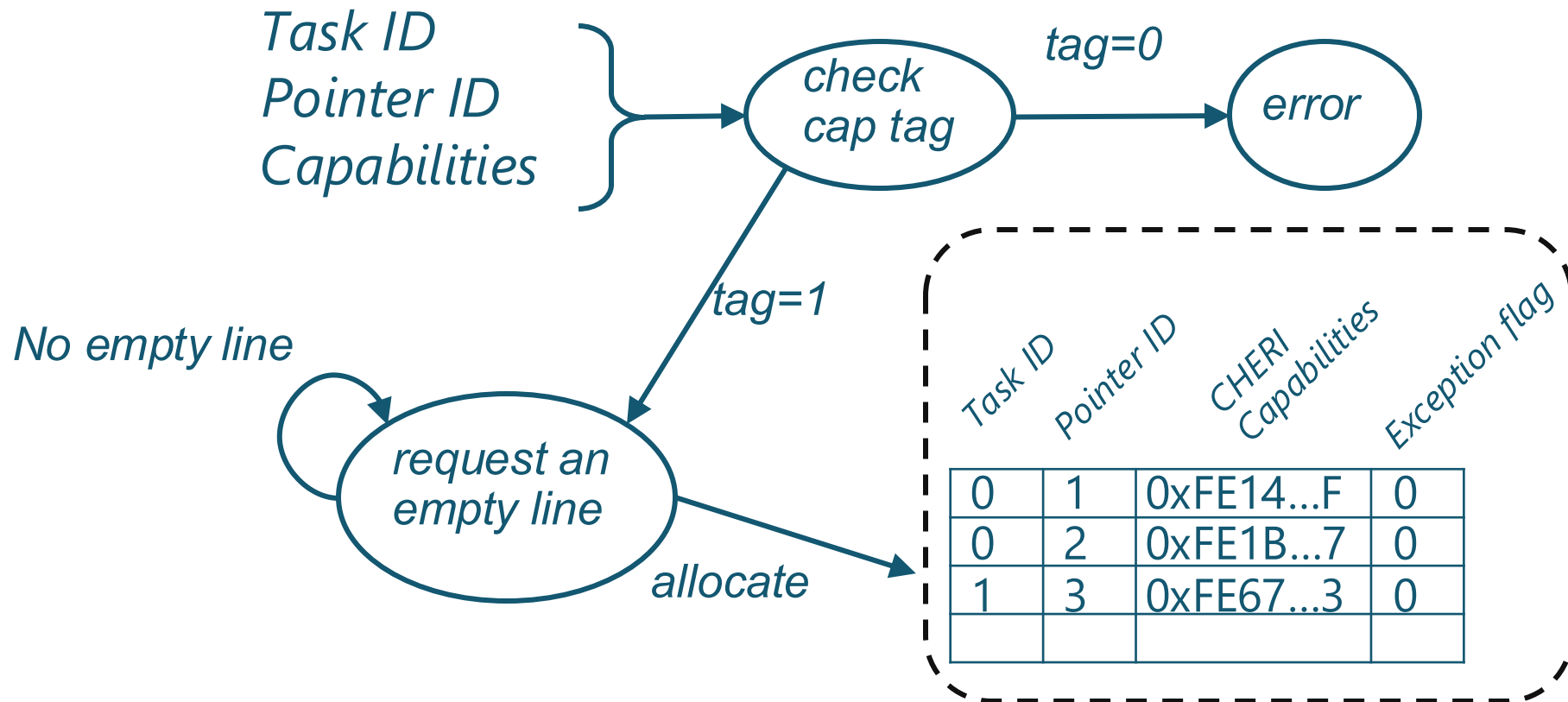
CapChecker Driver

Allocate capabilities



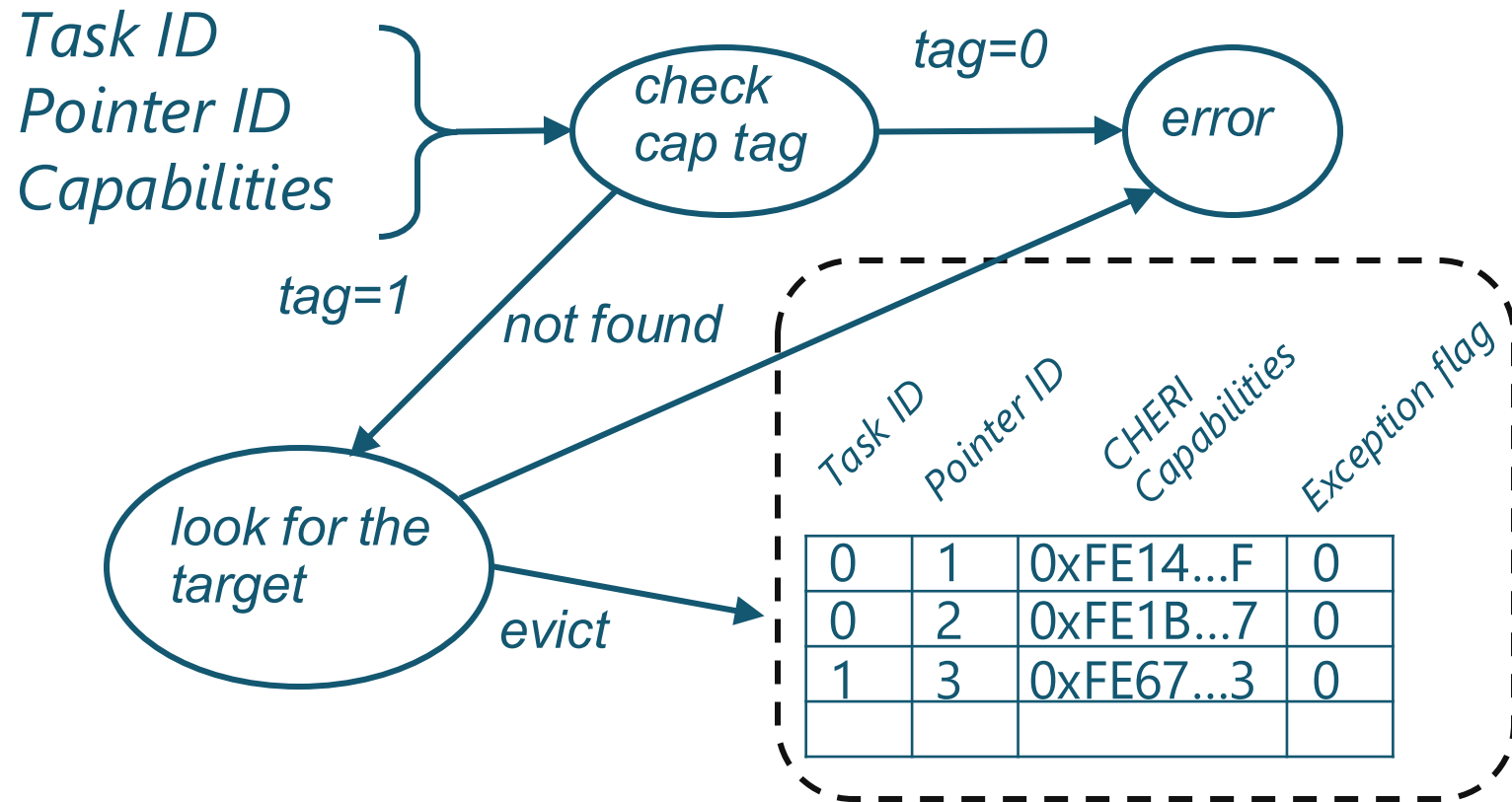
CapChecker Driver

Allocate capabilities



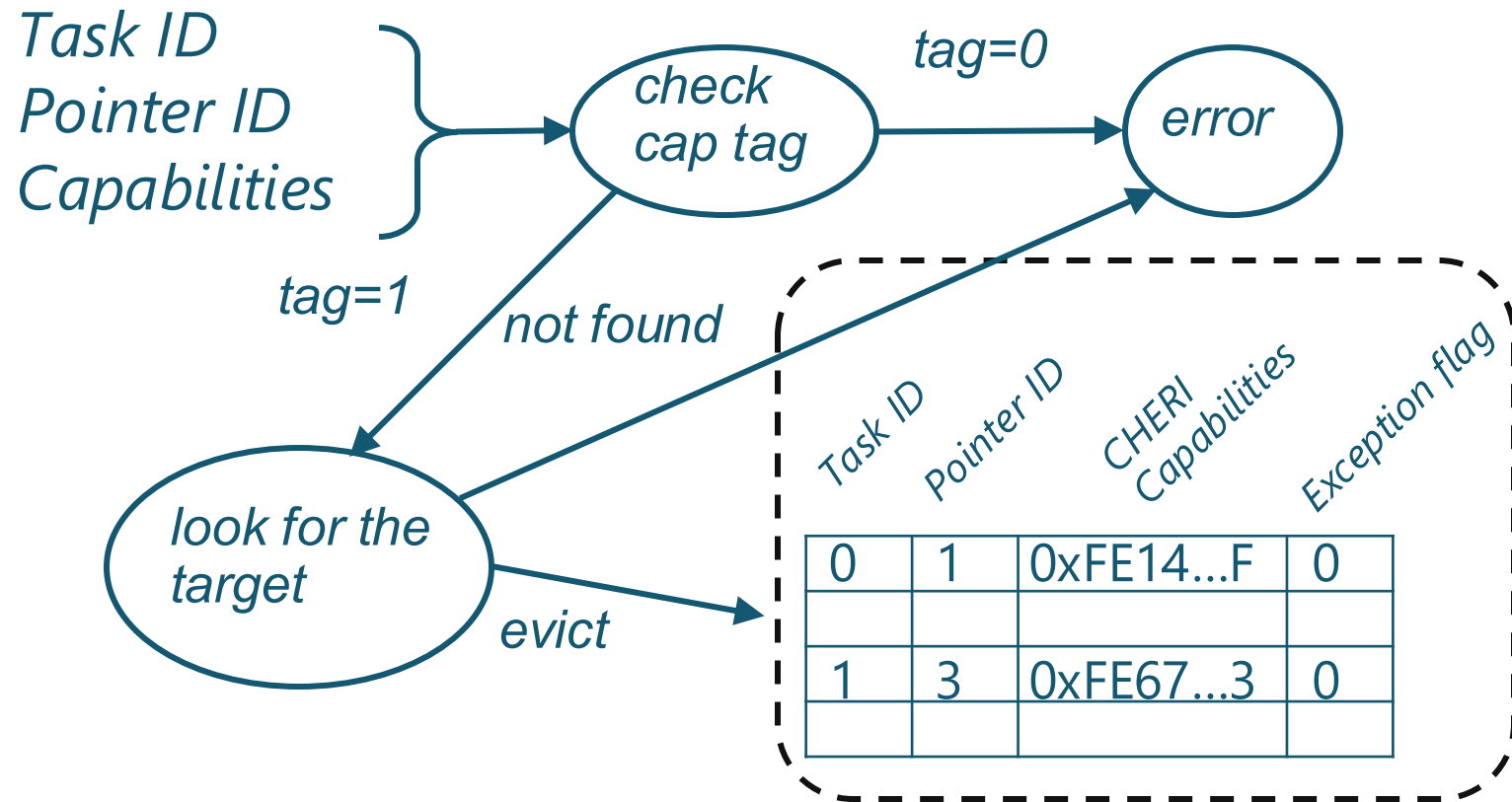
CapChecker Driver

Evict capabilities



CapChecker Driver

Evict capabilities





○ Experimental Evaluation

- Security
- Overhead



○ Security: CapChecker has the finest grain

- CWE Classification
- Metric: granularity

ID	Memory Weaknesses	No Method	IOMMU	sNPU	CapChecker
...	Buffer overreads or overwrites				
761	Free of Pointer not at Start of Buffer				
822	Untrusted Pointer Dereference				
823	Untrusted Pointer Offset				



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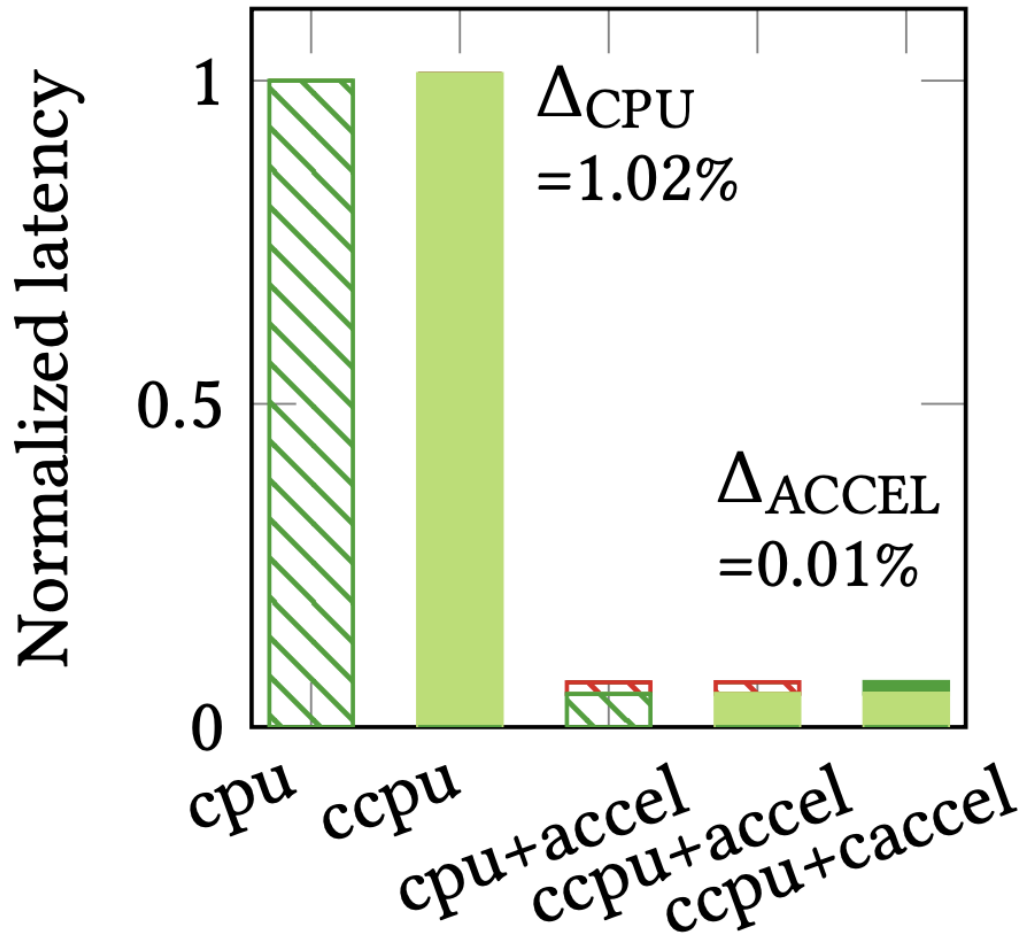


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823	Untrusted Pointer Offset	✗	✗	Task	Object

Performance: CapChecker has <5% overhead



- CHERI-unaware CPU
- CHERI-unaware accelerator
- CHERI-aware CPU
- CHERI-aware accelerator

CHERI overhead:

Accelerators < CPU

(d) fft_strided



○ Conclusion and Future Work

What we did

- Compartmentalization for heterogeneous systems
- CapChecker presents fine-grained hardware protection
- CapChecker is affordable and scalable

What we plan to do

- Dynamic memory managements
- Micro-architecture exploration



CHERI

THANK YOU

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Web www.cheri-alliance.org

Link to the paper:

